EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	46	("CASE, LAWRENCE" or "REDMAN, MARK" or "TKACIK, THOMAS" or "FELDMAN, JOEL")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:14
S2	61308	"MOTOROLA, INC"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:14
S3	23076	S2 and ("controller" or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:25
S4	4	S3 and (compar$3 with (("real" ajd "time" adj "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:56
S5	3213002	(("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:34
S6	452	S5 and (compar$3 with (("real" ajd "time" adj "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:29
S7	4	S6 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:34
S8	300	726/22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:33
S9	258	S8 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:55
S10	0	S9 and (compar$3 with (("real" ajd "time" adj "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:34
S11	1	S9 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:56

EAST Search History

S12	3	"7114048"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:39
S13	12	"6009527"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:40
S14	14	"6701432"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:40
S15	9	"5864286"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/10 13:48
S16	2609	713/193	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:55
S17	2319	S16 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:58
S18	0	S17 and (compar$3 with (("real" ajd "time" adj "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:59
S19	7	S17 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:03
S20	1457	713/153	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 09:58
S21	1175	S20 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 11:57
S22	0	S21 and (compar$3 with (("real" ajd "time" adj "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:00
S23	0	S21 and (compar$3 with((("real" ajd "time") with "value") and ("reference" adj "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:27

EAST Search History

S24	0	S21 and (compar$3 with((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:29
S25	2	S21 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:30
S26	0	S21 and (compar$3 with((("real" ajd "time") with "data") and ("reference" adj "data")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 10:28
S27	0	S21 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 11:57
S28	18	S21 and (compar$3 with (("real" ajd "time") with "value"))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:01
S29	0	S28 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 11:57
S30	3036	713/168	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 11:56
S31	2566	S30 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:00
S32	6	S31 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:01
S33	0	S32 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:01
S34	1066	713/165	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:00
S35	932	S34 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:02

S36	0	S35 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:03
S37	13	S35 and (compar$3 with (("real" ajd "time") with "value"))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:03
S38	0	S37 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:03
S39	736	726/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:02
S40	630	S39 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:51
S41	0	S40 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:51
S42	15	S40 and (compar$3 with (("real" ajd "time") with "value"))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S43	0	S42 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:52
S44	3450	711/100	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:51
S45	3329	S44 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:53
S46	2	S45 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:53
S47	0	S46 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:53

S48	4527	711/170	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:53
S49	4447	S48 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:58
S50	4	S49 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:58
S51	0	S50 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:58
S52	1812	710/22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 12:58
S53	1775	S52 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S54	5	S53 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S55	0	S54 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S56	355	710/308	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S57	341	S56 and (("DMA" adj "controller") or ("controller") or "memory" or ("authentication" adj ("engine" or "unit" or "device" or "system")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S58	0	S57 and (compar$3 with ((("real" ajd "time") with "value") and ("reference" with "value")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03
S59	18	S57 and (compar$3 with (("real" ajd "time") with "value"))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:04

S60	0	S59 and (check$3 with "runtime")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/09/11 13:03

Google Patent Search BETA

computer with runtime mode | Search Patents | Advanced Patent Search
Google Patent Search Help

"with" is a very common word and was not included in your search. [details]

Patents

Patents **1 - 10** on **computer with runtime mode. (0.12** seconds)

Sort by relevance | Sort by date (new first) | Sort by date (old first)

Computer system and method of selectively rebooting the same in response to ...
US Pat. 6044461 - Filed Sep 16, 1997 - International Business Machines Corporation
Once the pending code updates are processed, the **computer** system is restarted in
the **runtime mode** and a selective reboot routine 130 is executed to ...

Computer telephone system
US Pat. 6026158 - Filed Apr 7, 1998 - Davox Corporation
15 **mode**, however, tool bar 108 may become a launcher used Examples of GUI ...
that contain the layout and behavior of **Computer** telephone system 10 may be ...

Storing valid and invalid markup language in strict and relaxed tables ...
US Pat. 6558431 - Filed Sep 11, 1998 - Macromedia, Inc.
The **computer** program product of claim 33 further modifications to the tree in
satisfying edit requests made in the simulated **runtime mode**; ...

Computer telephone system
US Pat. 5754636 - Filed Feb 21, 1997 - Answersoft, Inc.
In step 150, a for launching applications created with GUI object builder from
a client **computer** system 14 is interrupted. Should **mode**, however, tool bar ...

Computer telephone system and method having a graphical user interface
US Pat. 6192118 - Filed Apr 7, 1998 - Davox Corporation
These definition files may be input directly in the run time **mode** of the GUI ...
Single User Database Synchronization 65 **Computer** telephone system 10 may be ...

Two computers cooperating via interconnected busses
US Pat. 5999997 - Filed Jul 26, 1996 - Compaq Computer Corporation
3A and 3B show a block diagram of a portable **computer** docked to a server and
operating in a run time **mode**. FIGS. 4A and 4B show a block diagram of a ...

Computer telephone system
US Pat. 6424711 - Filed Jan 20, 1999 - Davox Corporation
During design **mode**, designer 138 creates an application by defining ... Single User
Database Synchronization **Computer** telephone system 10 may be used with a ...

Computer telephone system
US Pat. 5991382 - Filed Apr 7, 1998 - Davox Corporation
During design **mode**, designer 138 creates an application by defining objects ...
stream of database requests from a client **computer** system 14 is interrupted. ...

Computer telephone system
US Pat. 6047054 - Filed Apr 7, 1998 - Davox Corporation
In design **mode**, from a client **computer** system 14 is interrupted. Should tool bar
108 shows low level objects used for design as transmission of a database ...

Distributed power regulation in a portable **computer** to optimize heat ...
US Pat. 5532524 - Filed May 11, 1994 - Apple Computer, Inc.
3, a state diagram of a power **mode** hierarchy of the **computer** system is illustrated.
... The **computer** system exists in Run **mode** 30 when fully operational and ...

computer with runtime mode | Search Patents

Google Patent Search Help | Advanced Patent Search

Google Home - About Google - About Google Patent Search

Google Patent Search BETA

| fetch or hash with boot mode | | Search Patents | Advanced Patent Search
Google Patent Search Help |

"with" is a very common word and was not included in your search. [details]
Lowercase **"or"** was ignored. Try **"OR"** to search for either of two terms. [details]

Patents

Patents **11 - 20** on **fetch or hash with boot mode**. (0.06 seconds)

Sort by relevance | Sort by date (new first) | Sort by date (old first)

Generating a key hieararchy for use in an isolated execution environment
US Pat. 6760441 - Filed Mar 31, 2000 - Intel Corporation
The isolated access bus **mode** is also asserted on instruction pre-**fetch** and cache
... a cryptographic key storage 155, a cryptographic **hash** unit 157, ...

Managing a secure environment using a chipset in isolated execution **mode**
US Pat. 7085935 - Filed Sep 22, 2000 - Intel Corporation
The isolated access bus **mode** is also asserted on instruction pre-**fetch** and ...
a processor nub loader code and its digest (eg, cryptographic **hash**) value. ...

Controlling accesses to isolated memory using a memory controller for ...
US Pat. 6795905 - Filed Sep 29, 2000 - Intel Corporation
1A, includes a processor nub loader code and its digest while the processor 110
is in the isolated execution **mode** (eg, cryptographic **hash**) value. ...

Platform and method for generating and utilizing a protected audit log
US Pat. 7073071 - Filed Mar 31, 2000 - Intel Corporation
The isolated access bus 10 **mode** is also asserted on instruction pre-**fetch** and
cache write-back cycles if the address is within the isolated area address ...

Attestation key memory device and bus
US Pat. 7013481 - Filed Mar 31, 2000 - Intel Corporation
The isolated access bus **mode** is also asserted on instruction pre-**fetch** and cache
... includes a processor nub loader code and its digest (eg, **hash**) value. ...

Platform and method for remote attestation of a platform
US Pat. 6990579 - Filed Mar 31, 2000 - Intel Corporation
The isolated 30 access link **mode** is also asserted on instruction pre-**fetch** and
cache write-back cycles if the address is within the isolated area address ...

Managing a secure environment using a chipset in isolated execution **mode**
US Pat. 7013484 - Filed Mar 31, 2000 - Intel Corporation
The isolated access bus **mode** is also asserted on instruction pre-**fetch** and ...
1A, includes a processor nub loader code and its digest (eg, **hash**) value. ...

Managing a secure platform using a hierarchical executive architecture in ...
US Pat. 6941458 - Filed Sep 22, 2000 - Intel Corporation
an isolated access bus **mode** with corresponding interface signals for memory ...
a processor nub loader code and its digest (eg, cryptographic **hash**) value. ...

Protecting software environment in isolated execution
US Pat. 7082615 - Filed Sep 22, 2000 - Intel Corporation
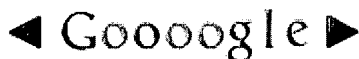The isolated access bus **mode** is also asserted on instruction pre-**fetch** and ...

1A, includes a processor nub loader code and its digest (eg, **hash**) value. ...

Generating isolated bus cycles for isolated execution
US Pat. 7111176 - Filed Mar 31, 2000 - Intel Corporation
The isolated access bus **mode** is also asserted on instruction pre-**fetch** and ...
1A, includes a processor nub loader code and its digest (eg, **hash**) value. ...

◀ Goooogle ▶

Result Page: **Previous** 1 2 3 4 **Next**

| fetch or hash with boot mode | Search Patents |

Google Patent Search Help | Advanced Patent Search

Google Home - About Google - About Google Patent Search

©2007 Google